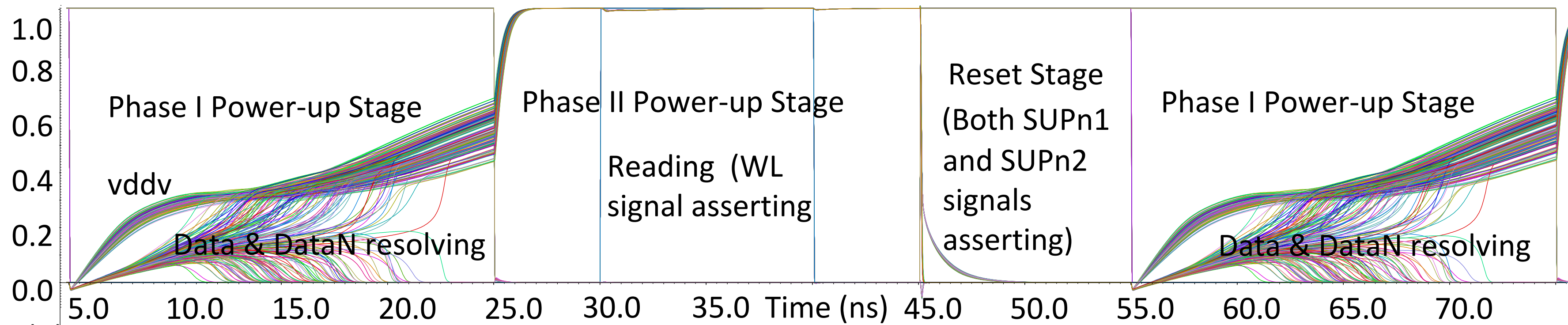


(a) Single-phase Power-gated 6T-SRAM PUF: SUPPLY transistor width = 1 $\mu$ m



(b) Two-phase Power-gated 8T PUF: transistor  $w_1 = 120\text{nm}$ ,  $w_2 = 5\mu\text{m}$