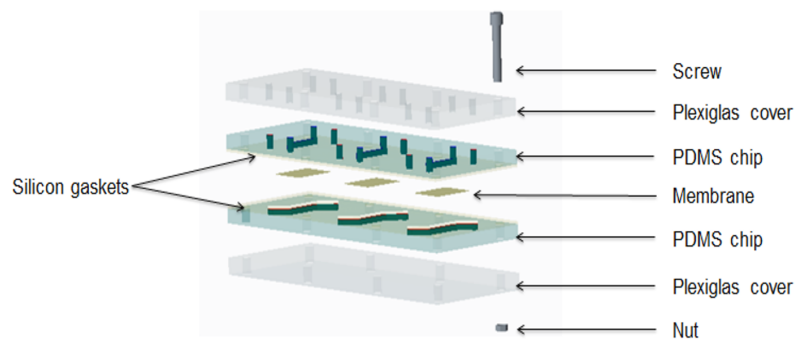
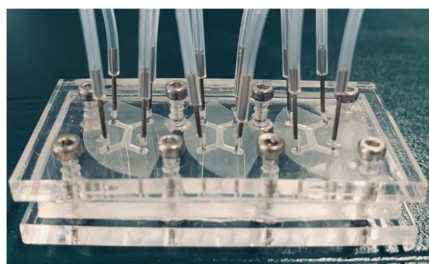


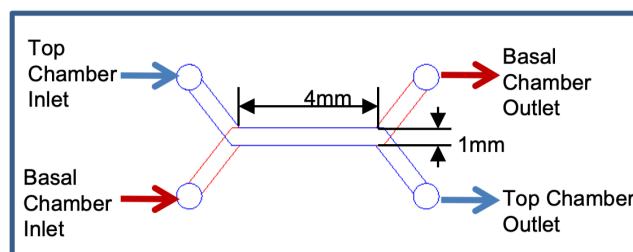
**A**



(i) Component of chip

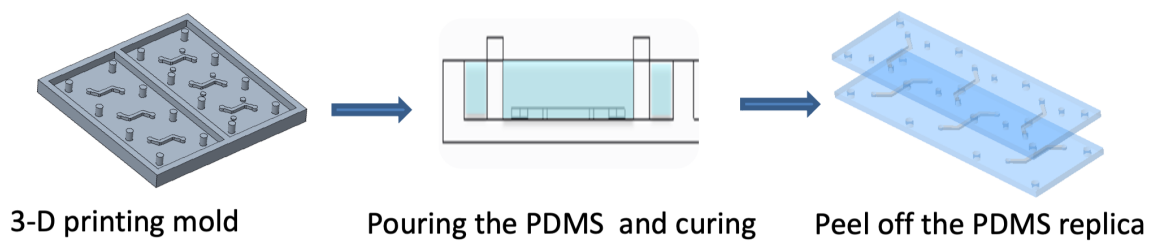


(ii) Assembled device

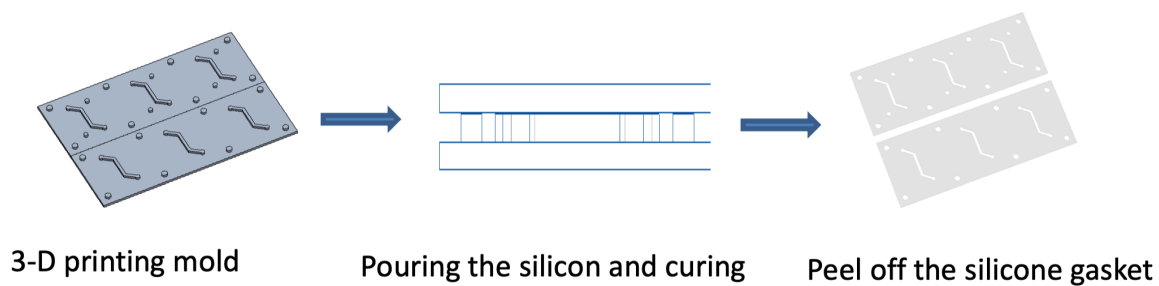


(iii) Layout and dimension of flow cell

**B**



**C**



**D**

