

Fig 1 System schematic diagram

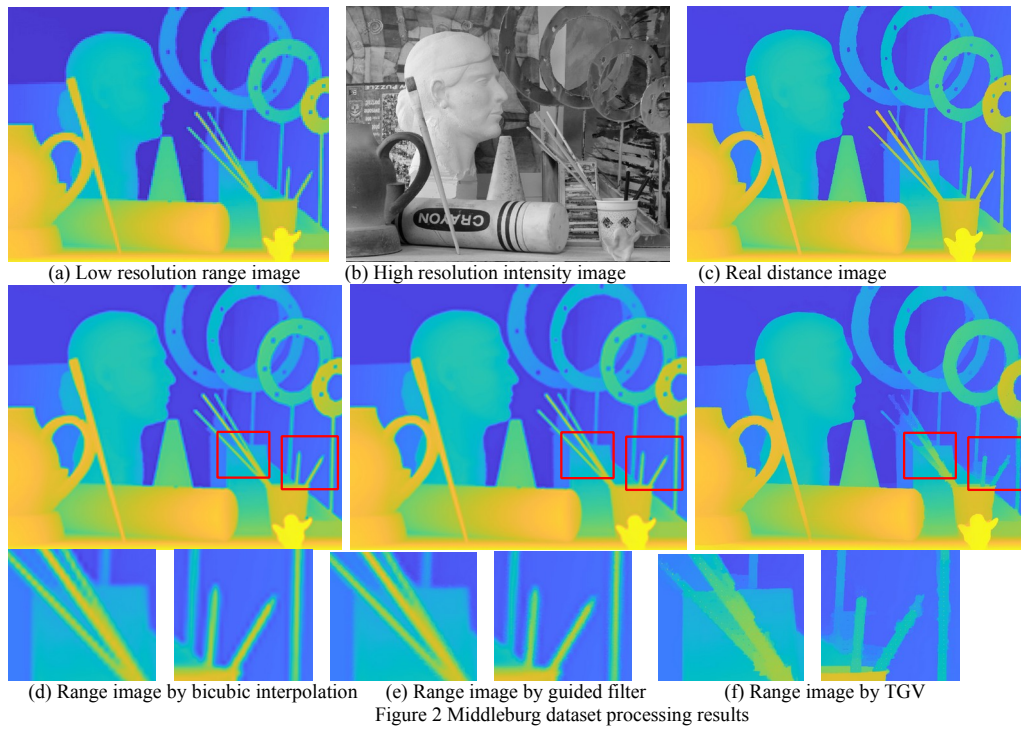


Figure 2 Middleburg dataset processing results

